



THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Krishna Seshan

Title: SELECTABLE DECOUPLING CAPACITORS FOR INTEGRATED CIRCUIT AND METHODS OF USE

Docket No.: 884.380US2

Serial No.: 10/726,050

Filed: December 2, 2003

Due Date: July 19, 2005

Examiner: David Vu

Group Art Unit: 2818

Customer No.: 21186

Confirmation No.: 2687

Commissioner for Patents

Notice of Allowance Date:

Attn: MAIL STOP ISSUE FEE

April 19, 2005

P.O. Box 1450

Alexandria, VA 22313-1450

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SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

By Ann M. McCrackin

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AMM1:CMG:dgw

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John D. Goltar-Wronski

Name

Signature



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Krishna Seshan	Examiner:	David Vu
Serial No.:	10/726,050	Group Art Unit:	2818
Filed:	December 2, 2003	Docket:	884.380US2
Title:	SELECTABLE DECOUPLING CAPACITORS FOR INTEGRATED CIRCUIT AND METHODS OF USE		
Assignee:	Intel Corporation	Customer No. 21186	

COMMUNICATION CONCERNING RELATED APPLICATION(S)

MS Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Applicant would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date</u>	<u>Attorney Docket</u>	<u>Title</u>
09/792596 6686659	February 23, 2001	884.380US1	SELECTABLE DECOUPLING CAPACITORS FOR INTEGRATED CIRCUIT (As Amended)

Continuations and divisionals may be later filed on the cases listed above, or cited to the Examiner in any previous Communication Concerning Related Applications. Applicant requests that the Examiner review all continuations and divisionals of the above-listed or previously-cited patent applications before allowing the claims of the present patent application.

Respectfully submitted,
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By his Representatives,
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Date July 19, 2005 By Ann M. McCrackin
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Reg. No. 42,858

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John D. Gwinther-Watson
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Signature